TRANSMITTAL **FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

		PTO/SB/21 (02-04)
Application Number	10/721,573	
Filing Date	November 24, 2003	
First Named Inventor	Low Yau Loong	
Art Unit	2825	
Examiner Name	Unassigned	
Attorney Docket Number	015114-068900US	

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ENCLOSURES (Check all that apply)							
Fee Tra	nsmittal Form	Drawing(s)	After Allowance Communication to Group				
	Fee Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences				
Extens Expres Informa Certifie Dócum Respor	After Final Affidavits/declaration(s) on of Time Request Abandonment Request tion Disclosure Statement d Copy of Priority ent(s) se to Missing Parts/ ete Application Response to Missing Parts under 37 CFR 1.52 or 1.53	Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer Request for Refund CD, Number of CD(s) Remarks	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please identify below): USPTO form SB08B (one page), copies of 4 cited references, Return Postcard				
	SIGN	ATURE OF APPLICANT, ATTORNEY, O	R AGENT				
Firm	Townsend and Town	nsend and Crew LLP					
1	or Individual name Steven Cahill Reg. No. 44,578						
Signature	Latered	ahill					
Date							
CERTIFICATE OF TRANSMISSION/MAILING							
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.							
Typed or printed name Roger Hylton							
Signature			Date 7/13/04				



ON 16 2004

Attorney Docket No.: 015114-068900US

Client Reference No.: A1187

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Low Yau Loong

Application No.: 10/721,573

Filed: November 24, 2003

For: OPTIMIZED TECHNOLOGY MAPPING TECHNIQUES FOR PROGRAMMABLE CIRCUITS

Examiner: To Be Assigned

Art Unit: 2825

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR §1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that <u>no fee is required</u> for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the Low Yau Loong

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undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Steven Cahill Reg. No. 44,578

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SJC:jey:r1h 60210274 v1

PTO/SB/08B (08-03) stitute for form 1449B/PTO Complete if Known Application Number 10/721,573 INFORMATION DISCLOSURE Filing Date November 24, 2003 STATEMENT BY APPLICANT First Named Inventor Low Yau Loong Art Unit 2825 (use as many sheets as necessary) Examiner Name Unassigned Sheet Attorney Docket Number 015114-068900US

		NON PATENT LITERATURE DOCUMENTS	
Examiner Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the iter (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
	1	"Command Line Scripting," Quartus II Handbook Volume 2, Design Implementation & Optimization, ch. 2, pp. 1-19, Altera Corporation 101 Innovation Drive San Jose, CA (2004).	
	2	"Quartus II Integrated Synthesis," Quartus II Handbook Volume 1, Design & Synthesis, ch. 8, pp. 1-28, Altera Corporation 101 Innovation Drive San Jose, CA pp. 8-1-(2004).	
-	3	"Quartus Programmable Logic Development System & Software," product Data Sheet, May 1999, ver. 1.01, Altera Corporation 101 Innovation Drive San Jose, CA (1999).	
	4	CHEN et al. "Performance-Driven Mapping for CPLD Architecture", Proc. ACM/SIGDA International Symposium on Field Programmable Gate Arrays, Monterey, California February 2001, pp. 39-47 (2001).	

Examiner	Date	
Signature	 Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance

and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.